

What is claimed is:

1. A power controller for receiving emission signals and outputting to an antenna, comprising:

5 a plurality of stage amplifiers for receiving the emission signals and amplifying the power thereof;

a plurality of matching circuits connected between the stage amplifiers for matching with the stage amplifiers, respectively;

at least one power detector for detecting the power of the stage amplifiers and generating detection signals, respectively; and

10 a bias control circuit for receiving the detection signals of the at least one power detector, thereby generating a bias of each of the stage amplifiers in order to optimize the efficiency of each of the stage amplifiers according to the magnitude of the power of each of the stage amplifiers.

15 2. The power controller according to claim 1, wherein the bias control circuit comprises a comparator, thereby outputting a bypass control signal when the power controller is disabled.

20 3. The power controller according to claim 2, further comprising a signal bypass unit, thereby grounding the emission signals for preventing the emission signals from being transmitted to each of the stage amplifiers when the bypass control signal is enabled.